

800.0039



see to 5
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Pechanek et al.

Serial No.: 09/422,015

Filed: October 21, 1999

For: METHODS AND APPARATUS
FOR ABBREVIATED INSTRUCTION
AND CONFIGURABLE PROCESSOR
ARCHITECTURE

Group: 2783

Examiner: Not Assigned Yet.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on the date set forth below:

Signed: Geraldine Miller

Name: Geraldine Miller

Date: December 1, 1999

Chapel Hill, North Carolina
December 1, 1999

Commissioner of Patents
and Trademarks
Attn: Box Missing Parts
Washington, DC 20231

**RESPONSE TO NOTICE TO FILE MISSING PARTS OF APPLICATION,
FILING DATE GRANTED**

Sir:

In response to the Notice to File Missing Parts of Application, Filing Date Granted dated November 12, 1999, we enclose a copy of said Notice and a Declaration and Power of Attorney for the above identified case. Also enclosed is our check for \$65 for the small entity fee surcharge.

Respectfully submitted,

Peter H. Priest

Peter H. Priest
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